PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

To Be Assigned

Filing Date:

To Be Assigned

Applicant:

Aram, Farbod et al.

Group Art Unit:

To Be Assigned

Examiner:

To Be Assigned

Title:

FRINGING CAPACITOR STRUCTURE

Attorney Docket:

MP0074.C1

Director of the United States Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant hereby submits an Information Disclosure Statement for consideration by the Examiner.

I. LIST OF PATENTS, PUBLICATIONS, AND OTHER INFORMATION

The patents, publications and other information submitted for consideration by the Office (except unpublished U.S. patent applications) are listed on Form 1449 attached hereto.

II. COPIES

- A. ___ Submitted herewith is a legible copy of (i) each U.S. patent application publication and U.S. and foreign patent; (ii) each publication or that portion which caused it to be listed; (iii) for each cited pending U.S. application, the application specification including the claims, and any drawing of the application which caused it to be listed including the claims directed to that portion; and (iv) all other information or that portion which caused it to be listed.
- B. X Any patents, publications or other information which are listed on Form 1449 or on the copies of PTO-892, but which are not enclosed herewith, were previously cited by or submitted to the PTO in one of the following applications which has been relied upon for an earlier filing date under 35 U.S.C. § 120:

U.S. Filing Date
January 18, 2001

C.____This is a PCT application in the entry of the National Phase in the United States. A copy of the International Search Report is attached for the Examiner's information. The documents listed on the International Search Report are listed on the attached Form-1449 for consideration by the Examiner and for listing on any patent resulting from this application. Since the International Search Report was from the US, EPO, or JPO search authorities, copies of these references should have been supplied to the USPTO under the trilateral agreement and are believed to be in the file of the above-identified application. (MPEP 1893.03(g))

III.	CONCISE EXPLANATION OF THE RELEVANCE	(check at least one box)

A. X Except as may be indicated below in (B), all of the patents, publications or other information are in the English language (concise explanation not required).
B A concise explanation of the relevance of each patent, publication or other information listed that is not in the English language is as follows (see 37 C.F.R. § 1.98(a)(3)):
 See the attached foreign patent office communication from a counterpart foreign application: English translations are provided for: Other:
C The following additional information is provided for the Examiner's

IV. CROSS REFERENCE TO RELATED APPLICATION(S)

A. X The Examiner is advised that the following co-pending application(s) contain(s) subject matter that may be related to the present application. By bringing this(these) application(s) to the Examiner's attention, Applicant(s) does(do) not waive the confidentiality provisions of 35 U.S.C. § 122.

<u>Serial No.</u> <u>Filing Date</u> <u>Art Unit</u> 09/765,200 January 18, 2001 2831

V. THIS IDS IS BEING FILED UNDER

consideration.

A. X 37 C.F.R. § 1.97(b): (check only one box)

1. X within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) (37 C.F.R. § 1.97(b)(1)). No fee or certification is required.

	2 within three months of the date of entry of the national stage as set forth in §1.491 in an international application (37 C.F.R. § 1.97(b)(2)). No fee or certification is required.
	3 before the mailing of a first Office Action on the merits (37 C.F.R. § 1.97(b)(3)). No fee or certification is required. In the event that a first Office Action on the merits has been issued, please consider this IDS under 37 C.F.R. § 1.97(c) and see the certification under 37 C.F.R. § 1.97(e) below; or, if no certification has been made, charge our deposit account a fee in the amount of \$180.00 as required by 37 C.F.R. § 1.17(p).
	4 before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. No fee or certification is required.
B	37 C.F.R. § 1.97(c): (check <u>only</u> one box)
	 before the mailing date of either any Final Office Action under 37 C.F.R. § 1.113, a Notice of Allowance under 37 C.F.R. § 1.311, or an action that otherwise closes prosecution.
	1 No certification; therefore, a fee in the amount of \$180.00 is required by 37 C.F.R. § 1.17(p).
	2 See the certification below. No fee is required.
C. ₋	37 C.F.R. § 1.97(d):
	 after the mailing date of either a Final Office Action under 37 C.F.R. § 1.113 or a Notice of Allowance under 37 C.F.R. § 1.311, yet on or before payment of the issue fee.
	1 See the certification below. A fee in the amount of \$180.00 is required by 37 C.F.R. § 1.17(p).
<u>CE</u>	RTIFICATION UNDER 37 C.F.R. § 1.97(e): (check only one box)
The	e undersigned hereby certifies that:
	each item of information contained in this IDS was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this IDS (See 37 C.F.R. § 1.97(e)(1)). See further statement under 37 C.F. R. 1.704(d) below in section VII. if applicable: or

VI.

- B. ___ no item of information contained in this IDS was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, no item of information contained in this IDS was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this IDS (See 37 C.F.R. § 1.97(e)(2)).
- C. __Some of the items of information were first cited in a communication from a foreign patent office. As to this information, the undersigned hereby certifies that each item of information contained in this IDS was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this IDS. As to the remaining information, the undersigned hereby certifies that no item of this remaining information contained in this IDS was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, no item of information contained in this IDS was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this IDS.

VII. STATEMENT UNDER 37 CFR 1.704(d)

The undersigned hereby states that:

____ each item of information contained in this IDS was cited in a communication from a foreign patent office in a counterpart application and this communication was not received by any individual designated in 37 C.F.R. 1.56(c) more than thirty days prior to the filing of this IDS.

VIII. PAYMENT OF FEES (check only one box)

- A. ___ A check in the amount of \$180.00 is enclosed for the above-identified fee.
- B. ___ Please charge Deposit Account No. 08-0750 in the amount of \$180.00 for the above-indicated fee. A duplicate copy of this paper is attached.

The above references are being cited only in the interest of candor and without any admission that they constitute statutory prior art, contain matter which anticipates the invention, or which would render the same obvious, either singly or in combination, to a person of ordinary skill in the art. Furthermore, this Information Disclosure Statement shall not be construed as a representation that a search has been made.

If it is determined that this IDS has been filed under the wrong rule, the PTO is requested to consider this IDS under the proper rule (with a petition if necessary) and charge the appropriate fee to Deposit Account No. 08-0750.

Please charge any additional fees or credit any overpayment pursuant to 37 C.F.R. § 1.16 or § 1.17 to Deposit Account No. 08-0750.

Respectfully submitted,

Michael D. Wiggins Reg. No. 34,754

MARVELL SEMICONDUCTOR, INC. 700 FIRST AVENUE, MAIL STOP 509 SUNNYVALE, CA 94089 408-222-2500

FORM HDP-1449 (Based on Form PTO-1449)

PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Sheet 1 of 2

ATTORNEY DOCKET NO.	SERIAL NO.
MP0074.C1	To Be Assigned
APPLICANT	
Aram, Farbod et al.	
FILING DATE	GROUP
To Be Assigned	To Be Assigned

U.S. P	U.S. PATENT DOCUMENTS					
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.		5,583,359	12-1996	Ng et al.		
2.		5,903,493	05-1999	Lee		
3.		6,001,702	12-1999	Cook et al.		
4.		6,150,707	11-2000	Cook et al.		
5.		6,266,226	07-2001	Hayashi		
6.		6,266,227	07-2001	Konushi et al.		
7.		6,312,963	11-2001	Chou et al.		
8.		6,414,806	07-2002	Gowda et al.		
9.		6,445,056	09-2002	Nakashima		
10.		6,459,561	10-2002	Galvagni et al.		
11.		6,501,610	12-2002	Sugawara et al.		

FOREIGN PATENT DOCUMENTS							
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes	n No
1.							

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)		
Ref. Desig.	Examiner's Initials	
1.		Bhupendra K. Ahuja, "An Improved Frequency Compensation Technique for CMOS Operational Amplifiers", IEEE J. Solid-State Circuits, vol. SC 18, no. 6, pp. 629-633, Dec. 1983.

Examiner:	Date Considered:

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(Use several sheets if necessary)

Sheet 2 of 2

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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)			
Ref. Desig.	Examiner's Initials		
2.		Runsheng He, Nersi Nazari and Sehat Sutardja, "A DSP Receiver for 1000BASE-T PHY," Submitted to ISSCC 2001.	
3.		S. Sutarja and P.R. Gray, "A pipelined 13-bit, 250-ks/s, 5-V, analog-to-digital converter," IEEE J. Solid-State Circuits, vol. 23, pp. 1316-1323, Dec. 1988.	
4.		S.H. Lewis and P.R. Gray, "A pipelined 5-Msample/s 9-bit analog to digital converter," IEEE J. Solid-State Circuits, vol. SC-22, pp. 954-961, Dec. 1987.	
5.		S.H. Lewis, R. Ramachandran "A 10-b 20-Msample/s Analog-to-Digital Converter," IEEE J. Solid-State Circuits, vol. 27, pp. 351-358, March 1992.	

Examiner: Date Considered:	
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